

UPCONVERTER/BPSK MODULATOR

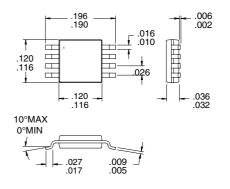
Typical Applications

- PCS/Cellular Systems
- PHS 1500/ WLAN 2400 Systems
- General Purpose Upconverter

- BPSK Modulation
- Micro-Cell PCS Base Stations
- Portable Battery Powered Equipment

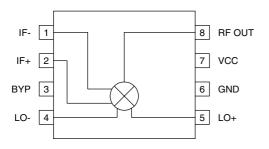
Product Description

The RF2639 is a complete upconverter designed for cellular and PCS applications. This device may also be used to directly BPSK modulate a carrier. The unit operates at 3.0V and is designed as part of the RFMD PCS/Cellular CDMA Chip Set, consisting of a Transmit IF AGC Amp, this Transmit Upconverter, a Receive LNA/Mixer, and a Receive IF AGC Amp.



Optimum Technology Matching® Applied

Si BJT	GaAs HBT	GaAs MESFET
Si Bi-CMOS	SiGe HBT	Si CMOS



Functional Block Diagram

Package Style: MSOP-8

Features

- Supports Dual Mode Operation
- +2dBm Output Intercept Point
- Single 3.0V Power Supply
- Miniature 8 Pin Package
- Double-Balanced Mixer

Ordering Information

RF2639 RF2639 PCBA

Upconverter/BPSK Modulator Fully Assembled Evaluation Board

 RF Micro Devices, Inc.
 Tel (336) 664 1233

 7625 Thorndike Road
 Fax (336) 664 0454

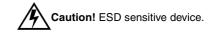
 Greensboro, NC 27409, USA
 http://www.rfmd.com

MIXERS

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Absolute Maximum Ratings

Parameter	Rating	Unit
Supply Voltage	-0.5 to +5.0	V _{DC}
Input RF Power	+3	dBm
Operating Ambient Temperature	-30 to +80	C°
Storage Temperature	-30 to +150	°C



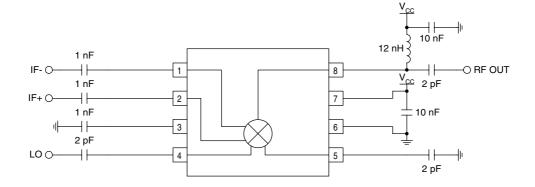
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Parameter	Specification		Unit	Condition		
Farameter	Min.	Тур.	Max.	Unit	Condition	
Overall					T=25°C, V _{CC} =3.0V	
RF Output Frequency Range		500 to 2500		MHz		
Conversion Gain		-1.0		dB	RF out=830MHz	
		-3.0		dB	RF out=1830MHz	
Noise Figure		11		dB	RF out=830MHz	
		11		dB	RF out=1830MHz	
Output IP3		+1		dBm	P _{IN} =-23dBm per Tone, RF out=830MHz	
		-1		dBm	P _{IN} =-23dBm per Tone, RF out=1830MHz	
Spurious Product Rejection		30		dBc	Referenced to RF output	
IF Input						
IF Frequency		DC to 300		MHz		
Differential Input Impedance		260		Ω		
IF to RF Output Isolation		30		dB		
IF to LO Isolation		30		dB		
LO Input						
LO Frequency Range		300 to 2700		MHz		
LO Level		-6 to 0		dBm		
LO to RF Output Leakage		-20		dBm		
RF to LO Isolation		30		dB		
LO Input VSWR		2:1			50Ω	
Power Supply						
Voltage		2.7 to 3.3		V		
Current Consumption		7		mA		

RF2639

Pin	Function	Description	Interface Schematic
1	IF-	Balanced IF Input Pin. This pin is internally DC biased and should be DC blocked if connected to a device with a DC level present. For single-ended input operation, one pin is used as an input and the other IF input is AC coupled to ground. The balanced, as well as single-ended, input impedance is 260Ω .	BIAS 130 Ω IF- IF- IF+
2	IF+	Same as pin 1, except complementary input.	See Pin 1.
3	ВҮР	Bypass pin for internal bias circuitry. Bypass with a 1 nF capacitor.	BYP J
4	LO-	Balanced LO Input Pin. This pin is internally DC biased and should be DC blocked if connected to a device with a DC level present. For single- ended input operation, one pin is used as an input and the other LO input is AC coupled to ground.	BIAS LO- LO- LO+
5	LO+	Same as pin 4, except complementary input.	See Pin 4.
6	GND	Ground connection. Keep traces physically short and connect immedi- ately to ground plane for best performance.	
7	VCC	Supply Voltage pin. External bypassing is required. External RF, LO, and IF bypassing is required. The trace length between the pin and the bypass capacitors should be minimized. The ground side of the bypass capacitors should connect immediately to ground plane.	
8	RF OUT	RF Output Pin.	

836 MHz Application Schematic

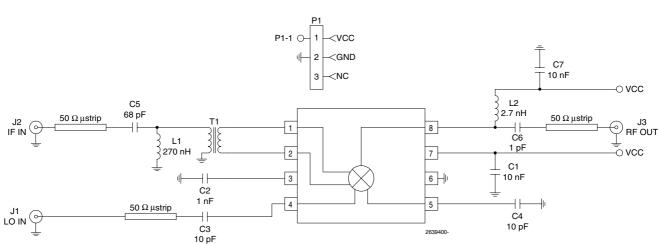


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V_{cc} 4 ++10 nF 2.7 nH 1 nF IF- O- \dashv 1 8 ++-O RF OUT V_{cc} 1 pF 1 nF IF+ O--| |-2 7 1 nF ⊥__ 10 nF —| |-⊪ 3 6 10 pF LO ()--- \dashv 4 5 +++10 pF

1830 MHz Application Schematic

Evaluation Board Schematic

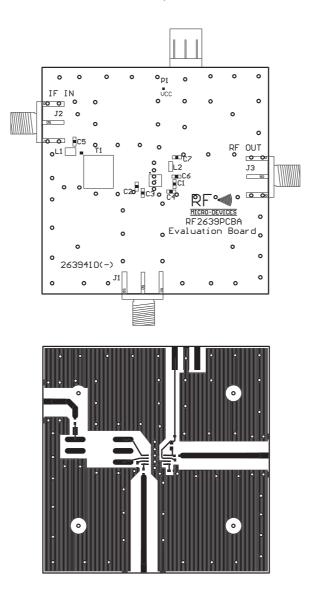


(Download Bill of Materials from www.rfmd.com.)



Evaluation Board Layout Board Size 2.0" x 2.0"

Board Thickness 0.031", Board Material FR-4



RF2639

Rev A6 991116