



## GRF2042

### LINEAR GAIN BLOCK WITH BYPASS

0.15 to 6 GHz

RELEASE Ø DATA SHEET

#### FEATURES

- Flexible Bias Voltage and Current
- Process: GaAs pHEMT
- Single Control Logic Input
- Internally Matched to 50  $\Omega$
- Compact 1.5 x 1.5 mm DFN-6 Package
- RoHS Compliant

**Reference: 5 V / 80 mA / 1.7 GHz**

- Gain: 15.5 dB
- OP1dB: 22 dBm
- OIP3: 38 dBm
- Evaluation Board Noise Figure: 2.1 dB
- Bypass Mode Gain: -1.6 dB
- Bypass Mode OP1dB: 22 dBm
- Bypass Mode OIP3: 45 dBm

#### APPLICATIONS

- Cellular Repeaters and Signal Boosters
- Cellular Infrastructure
- VHF/UHF and ISM Radios

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#### DESCRIPTION

The GRF2042 is a broadband gain block with bypass for small cell, wireless infrastructure, and other high-performance applications. It exhibits flat gain, outstanding Noise Figure (NF), and linearity over 400 to 2700 MHz with a single match.

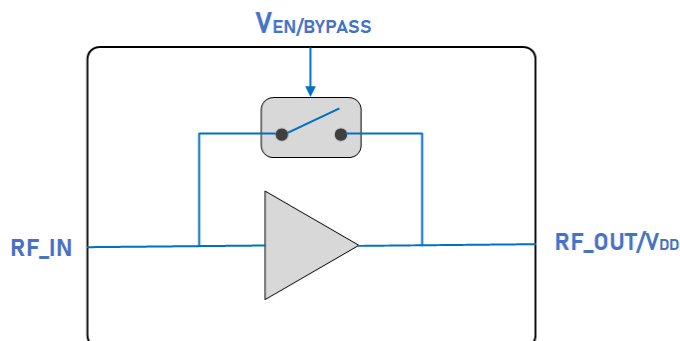
The device can be operated from a supply voltage of 2.7 to 6 V with a selectable  $I_{DDQ}$  range from 20 to 100 mA for optimal efficiency and linearity.

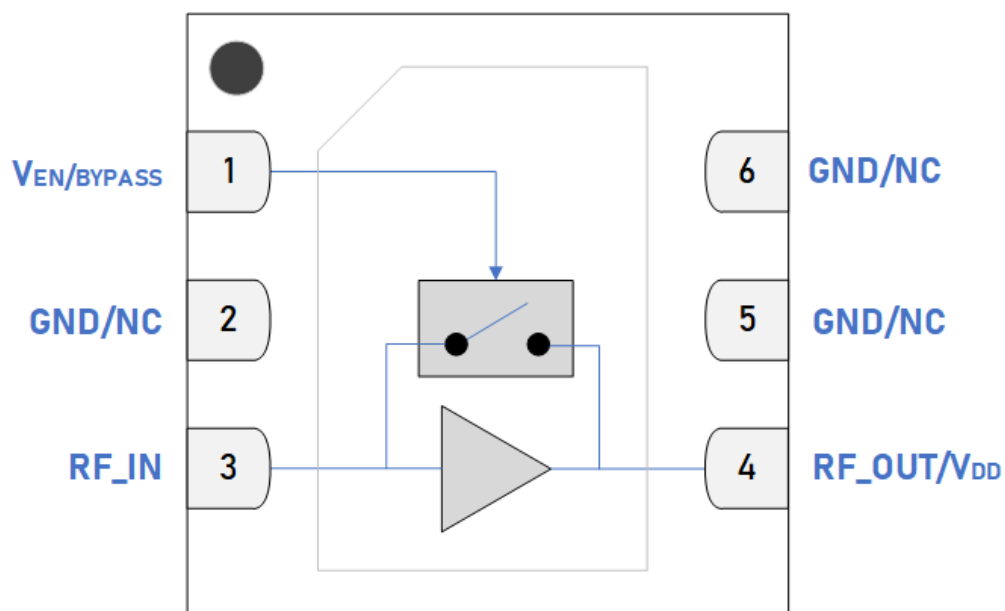
The GRF2042 is internally matched to 50  $\Omega$  at the input and output ports needing only external DC blocks and a bias choke on the output.

Please consult with the GRF applications engineering team for custom tuning/evaluation board data and device s-parameters.

Additional tunes can be found on the GRF2042 "Custom Tunes" product page: [GRF2042 Custom Tunes](#)

#### BLOCK DIAGRAM





Pin Out (Top View)

## Pin Assignments

Pin	Name	Description	Note
1	V <sub>ENABLE/BYPASS</sub>	V <sub>EN/BYPASS</sub> Voltage Input	V <sub>EN/BYPASS</sub> ≤ 0.2 volts sets Bypass Mode. V <sub>EN/BYPASS</sub> and external series resistor control the device I <sub>DDQ</sub> when V <sub>EN/BYPASS</sub> is high.
2, 5, 6	GND/NC	Ground or No Connect	No internal connection to die. These pins can be left unconnected or connected to ground (highly recommended). Use a via as close to the pin as possible if grounded.
3	RF_IN	RF Input	Internally matched 50 Ω. An external DC-blocking capacitor must be used.
4	RF_OUT/V <sub>DD</sub>	RF Output	Internally matched 50 Ω. V <sub>DD</sub> must be applied to this pin through a RF Choke.
PKG BASE	GND	Ground	Provides DC and RF ground for device, as well as thermal heat sink. Recommend multiple 8 mil vias beneath the package for optimal RF and thermal performance. Refer to evaluation board top layer graphic on schematic page.

## Control Logic Truth Table

Mode	Description	V <sub>DD</sub>	V <sub>EN/BYPASS</sub>
High Gain	High Gain	1	1
Bypass	Linear Bypass Mode	1	0
Logic Level "0"	Logic Low	0 V to 0.2 V	0 V to 0.2 V
Logic Level "1"	Logic High	1.8 V to 5 V	1.5 to V <sub>DD</sub>

## Absolute Ratings

Parameter	Symbol	Min.	Max.	Unit
Supply Voltage	$V_{DD}$	0	6	V
RF Input Power: Load VSWR < 2:1, $V_{DD} = 5\text{ V}$ , $V_{EN/BYPASS} = 5\text{ V}$ , Gain Mode	$P_{IN\ MAX}$		27	dBm
RF Input Power: Load VSWR < 2:1, $V_{DD} = 5\text{ V}$ , $V_{EN/BYPASS} = 0\text{ V}$ , Bypass Mode	$P_{IN\ MAX}$		27	dBm
Operating Temperature (Package Base)	$T_{PKG\ BASE}$	-40	105	°C
Maximum Channel Temperature (MTTF > 10 <sup>6</sup> Hours)	$T_{MAX}$		170	°C
Maximum Dissipated Power	$P_{DISS\ MAX}$		600	mW

## Electrostatic Discharge

Human Body Model	HBM	250		V
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## Storage

Storage Temperature	$T_{STG}$	-65	150	°C
Moisture Sensitivity Level	MSL		1	--



**Caution! ESD Sensitive Device.**

**Exceeding Absolute Maximum Rating conditions may cause permanent damage.**

Note: For additional information, please refer to [Manufacturing Note MN-001 - Packaging and Manufacturing Information](#).



All Guerrilla RF products are provided in RoHS compliant lead (Pb)-free packaging. For additional information, please refer to the [Certificate of RoHS Compliance](#).

## Recommended Operating Conditions

Parameter	Symbol	Specification			Unit	Condition
		Min.	Typ.	Max.		
Supply Voltage	$V_{DD}$	2.7	5	6	V	
Operating Temperature Range	$T_{PKG\ BASE}$	-40		105	°C	
RF Frequency Range	$F_{RF}$	0.15	1.7	6	GHz	Typical application schematic with external matching components ( <b>note 1 &amp; 2</b> ).
RF_IN Port Impedance	$Z_{RFIN}$		50		$\Omega$	
RF_OUT Port Impedance	$Z_{RFOUT}$		50		$\Omega$	

**Note 1:** Operation outside of this range is supported by using different custom tunes. Examples of other optimized tunes can be found here: [GRF2042 Custom Tunes](#)

**Note 2:** Contact the Guerrilla RF applications team for guidance on optimizing the tuning of the device for alternative bands.

## Nominal Operating Parameters - General

The following conditions apply unless noted otherwise: typical application schematic using the 0.7 to 2.7 GHz tuning set.  $M5 = 60\ \Omega$ ,  $V_{DD} = 5\text{ V}$ ,  $V_{EN/BYPASS} = 5\text{ V}$ ,  $I_{DDQ} = 80\text{ mA}$ ,  $F_{TEST} = 1.7\text{ GHz}$ ,  $T_{PKG\ BASE} = 25\text{ }^{\circ}\text{C}$ . Evaluation board losses are included within the specifications.

Parameter	Symbol	Specification			Unit	Condition
		Min.	Typ.	Max.		
Supply Current	$I_{DDQ}$		80		mA	$V_{DD} = 5\text{ V}$ , $V_{EN/BYPASS} = 5\text{ V}$ .
Enable Current	$I_{ENABLE}$		5		mA	$V_{DD} = 5\text{ V}$ , $V_{EN/BYPASS} = 5\text{ V}$ .
Switching Rise Time	$T_{RISE}$		200		ns	Bypass Mode to Gain Mode. Pin = 9 dBm <b>(note 3)</b> .
Switching Fall Time	$T_{FALL}$		50		ns	Bypass Mode to Gain Mode. Pin = 9 dBm <b>(note 4)</b> .

### Disabled Mode

Leakage Current	$I_{LEAKAGE}$		500		$\mu\text{A}$	$V_{DD} = 5\text{ V}$ , $V_{EN/BYPASS} = 0\text{ V}$ .
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### Thermal Data

Thermal Resistance: (Infrared Scan)	$\theta_{JC}$		75		$^{\circ}\text{C/W}$	On standard Evaluation Board <b>(note 5)</b> .
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**Note 3:** Switching Time: 50% of  $V_{EN/BYPASS}$  to 90% of  $P_{OUT}$ .

**Note 4:** Switching Time: 50% of  $V_{EN/BYPASS}$  to 10% of  $P_{OUT}$ .

**Note 5:** MTTF >  $10^6$  hours for  $T_{CHANNEL} \leq 170\text{ }^{\circ}\text{C}$ .

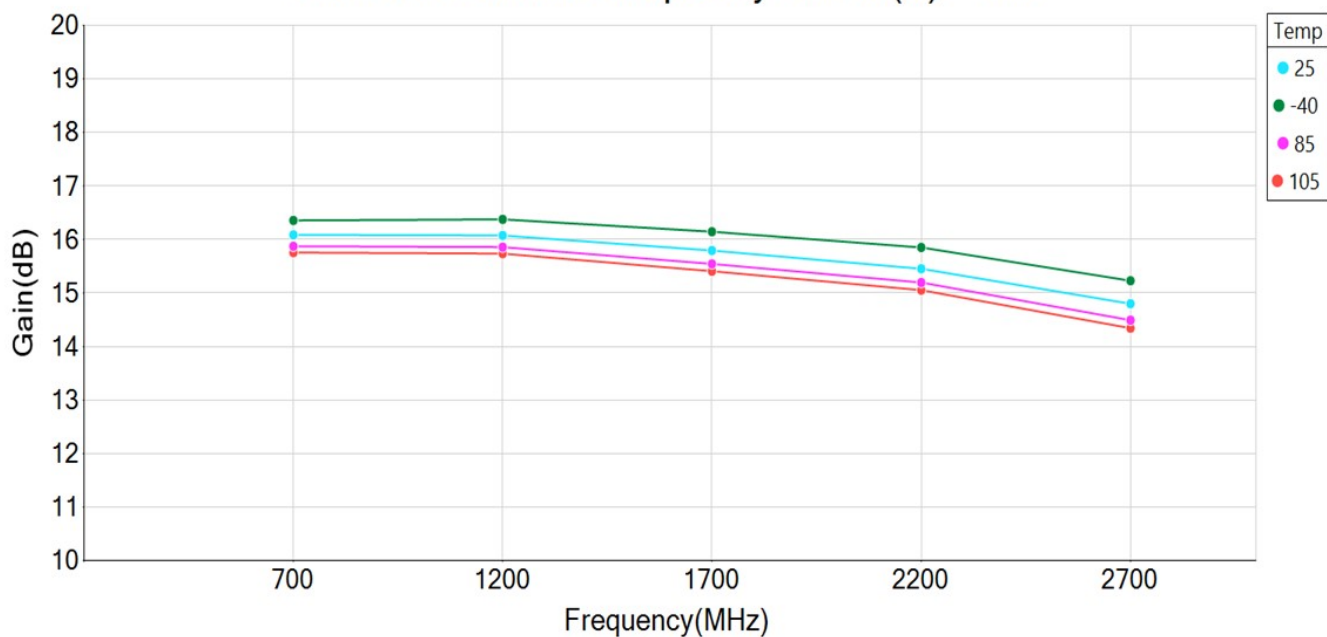
## Nominal Operating Parameters - RF

The following conditions apply unless noted otherwise: typical application schematic using the 0.7 to 2.7 GHz tuning set.  $M5 = 60\ \Omega$ ,  $V_{DD} = 5\text{ V}$ ,  $V_{EN/BYPASS} = 5\text{ V}$ ,  $I_{DDQ} = 80\text{ mA}$ .  $F_{TEST} = 1.7\text{ GHz}$ .  $T_{PKG\ BASE} = 25\text{ }^{\circ}\text{C}$ . Evaluation board losses are included within the specifications.

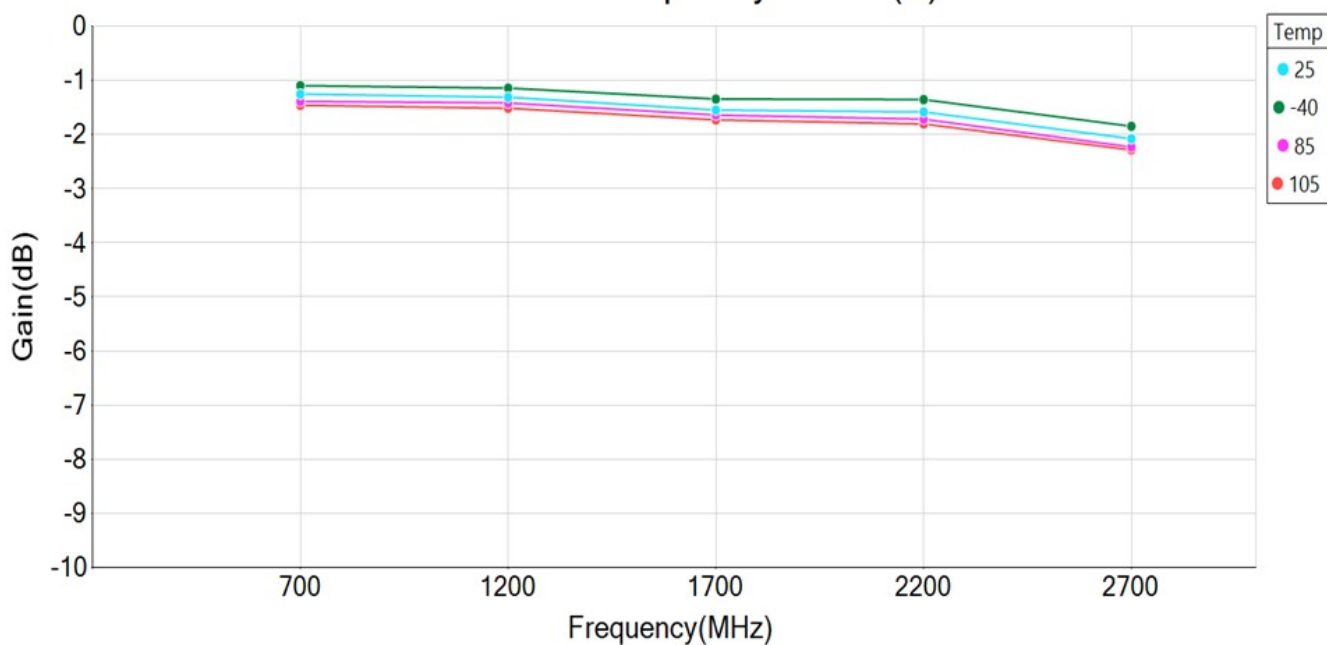
Parameter	Symbol	Specification			Unit	Condition
		Min.	Typ.	Max.		
High Gain Mode						V <sub>DD</sub> = 5 V, V <sub>EN/BYPASS</sub> = 5 V
Gain	S21	13.5	15.5		dB	
Output 3rd Order Intercept Point	OIP3		38		dBm	2 dBm P <sub>OUT</sub> per tone at 2 MHz spacing (1699 and 1701 MHz).
Output 1 dB Compression Power	OP1dB	20	22		dBm	
Evaluation Board Noise Figure	NF		2.1		dB	
Bypass Mode						V <sub>DD</sub> = 5 V, V <sub>EN/BYPASS</sub> = 0 V
Gain	S21	-3.5	-1.6		dB	
Output 3rd Order Intercept Point	OIP3		45		dBm	8 dBm P <sub>OUT</sub> per tone at 2 MHz spacing (1699 and 1701 MHz).
Output 1 dB Compression Power	OP1dB		22		dBm	

## GRF2042 Typical Operating Curves: 0.7 to 2.7 GHz Tune

GRF2042 Gain vs Frequency at  $V_{en}(V) = 5\text{ V}$



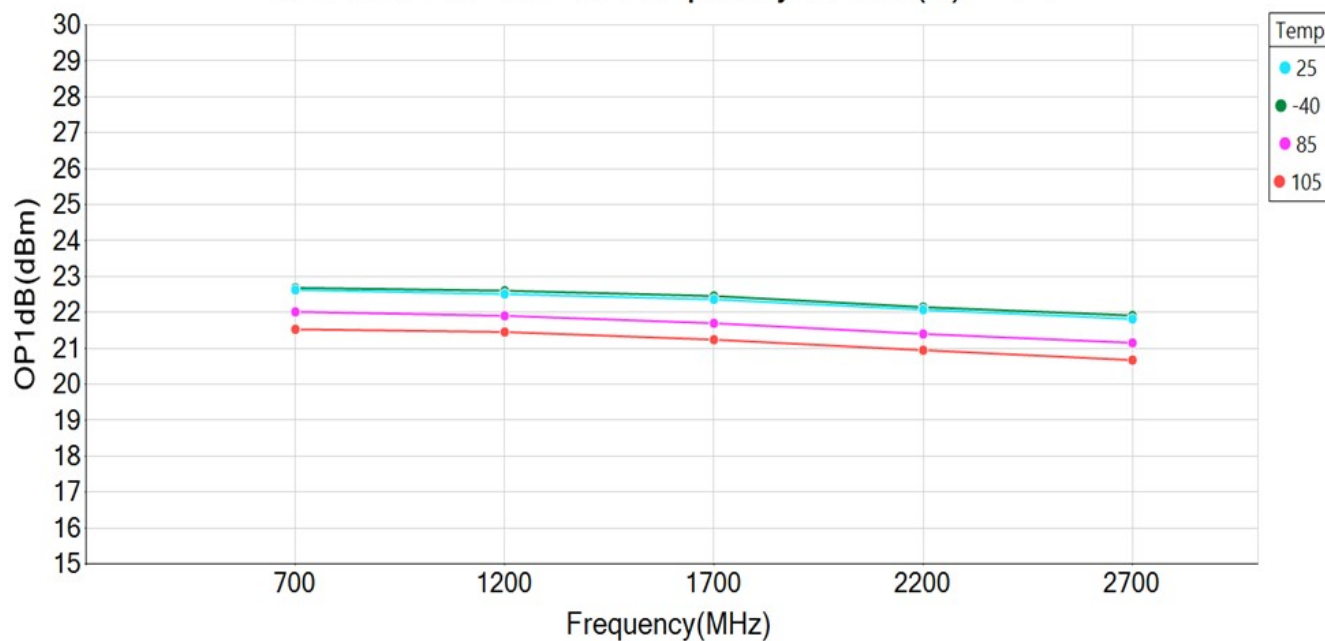
GRF2042 Gain vs Frequency at  $V_{en}(V) = 0\text{ V}$



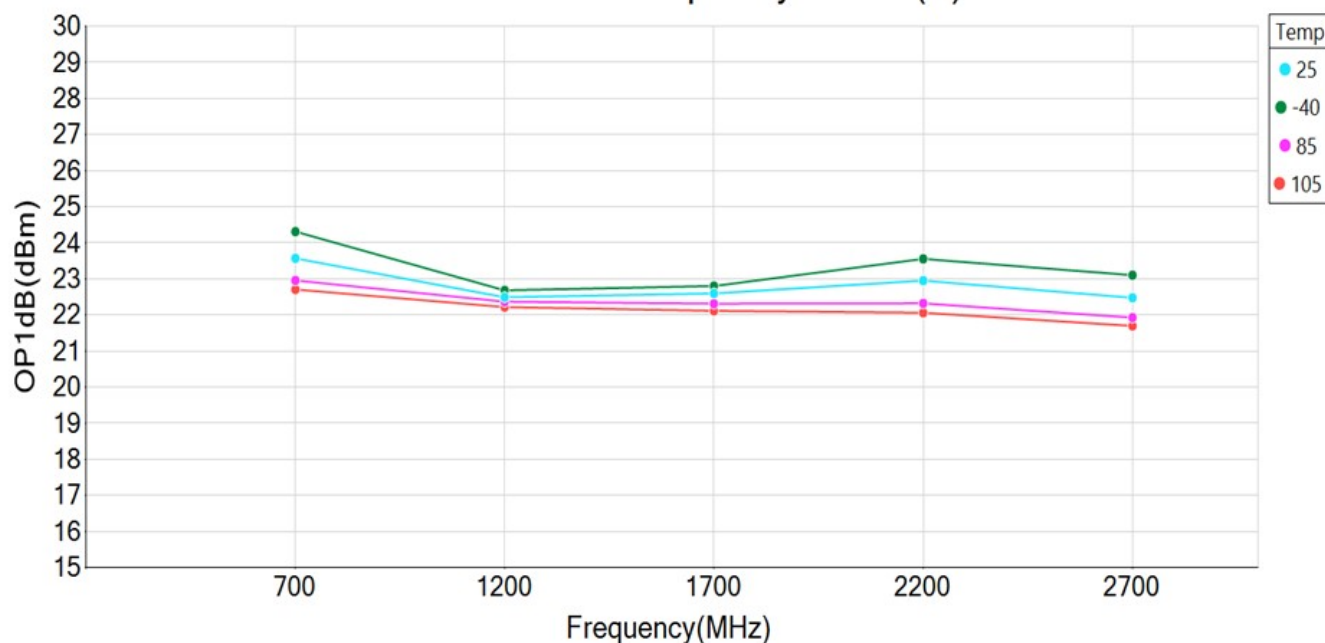


## GRF2042 Typical Operating Curves: 0.7 to 2.7 GHz Tune

GRF2042 OP1dB vs Frequency at  $V_{en}(V) = 5 V$

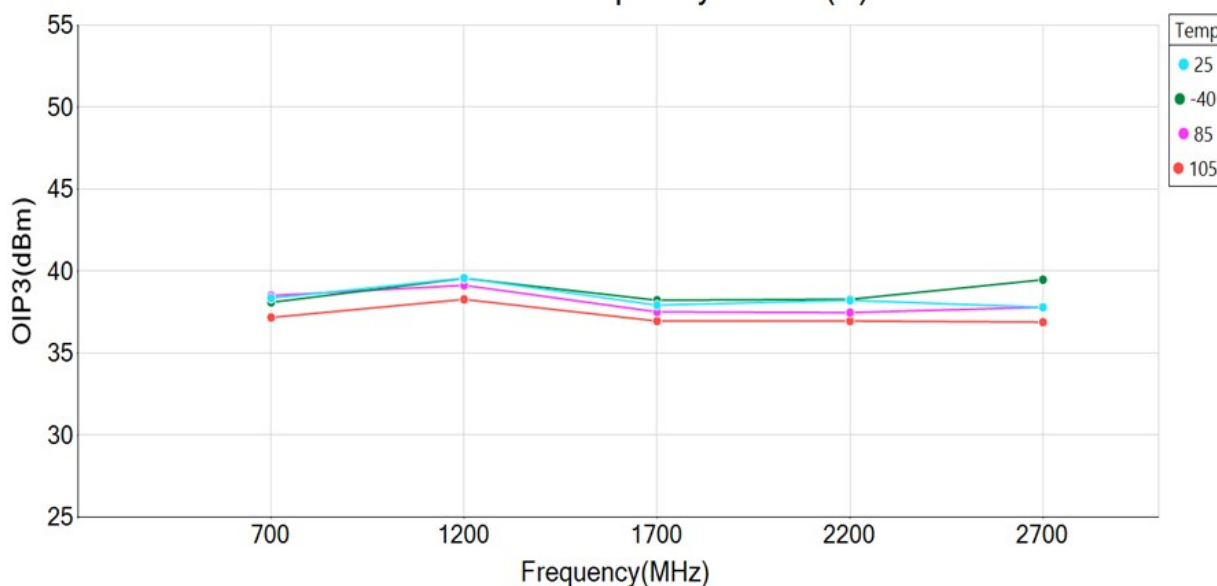


GRF2042 OP1dB vs Frequency at  $V_{en}(V) = 0 V$

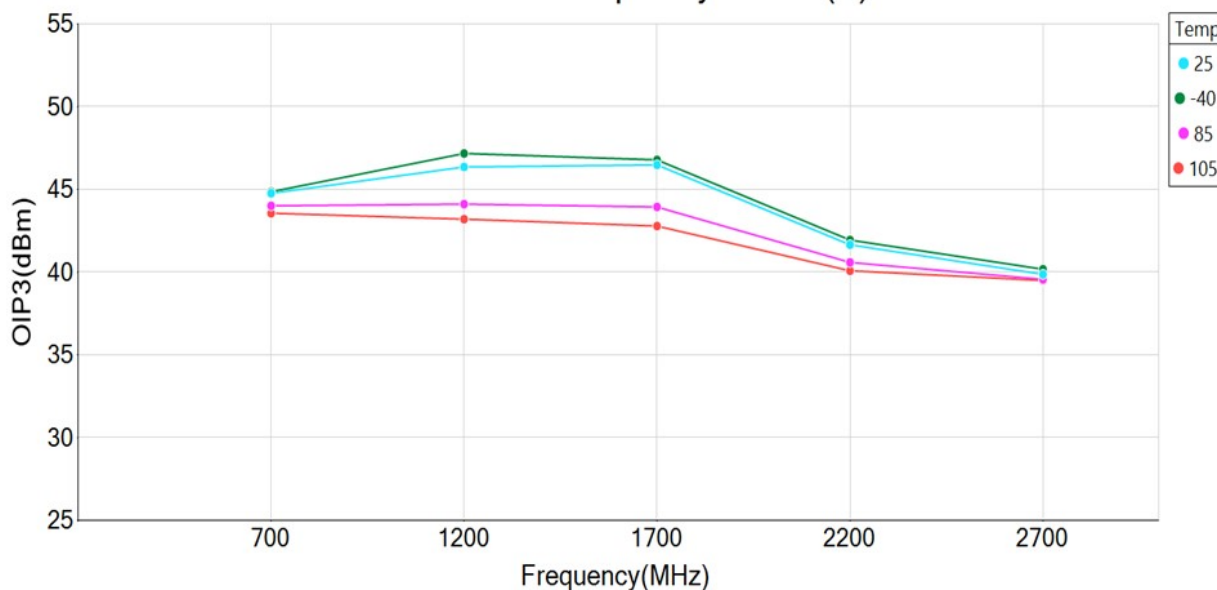


## GRF2042 Typical Operating Curves: 0.7 to 2.7 GHz Tune

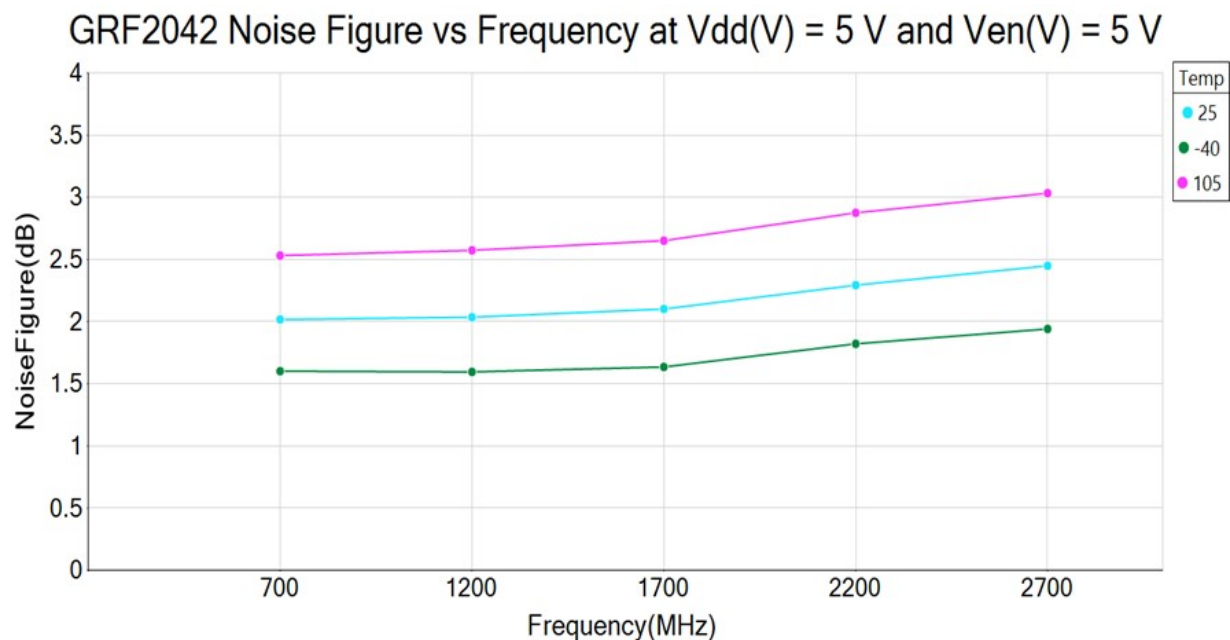
GRF2042 OIP3 vs Frequency at  $V_{en}(V) = 5 V$



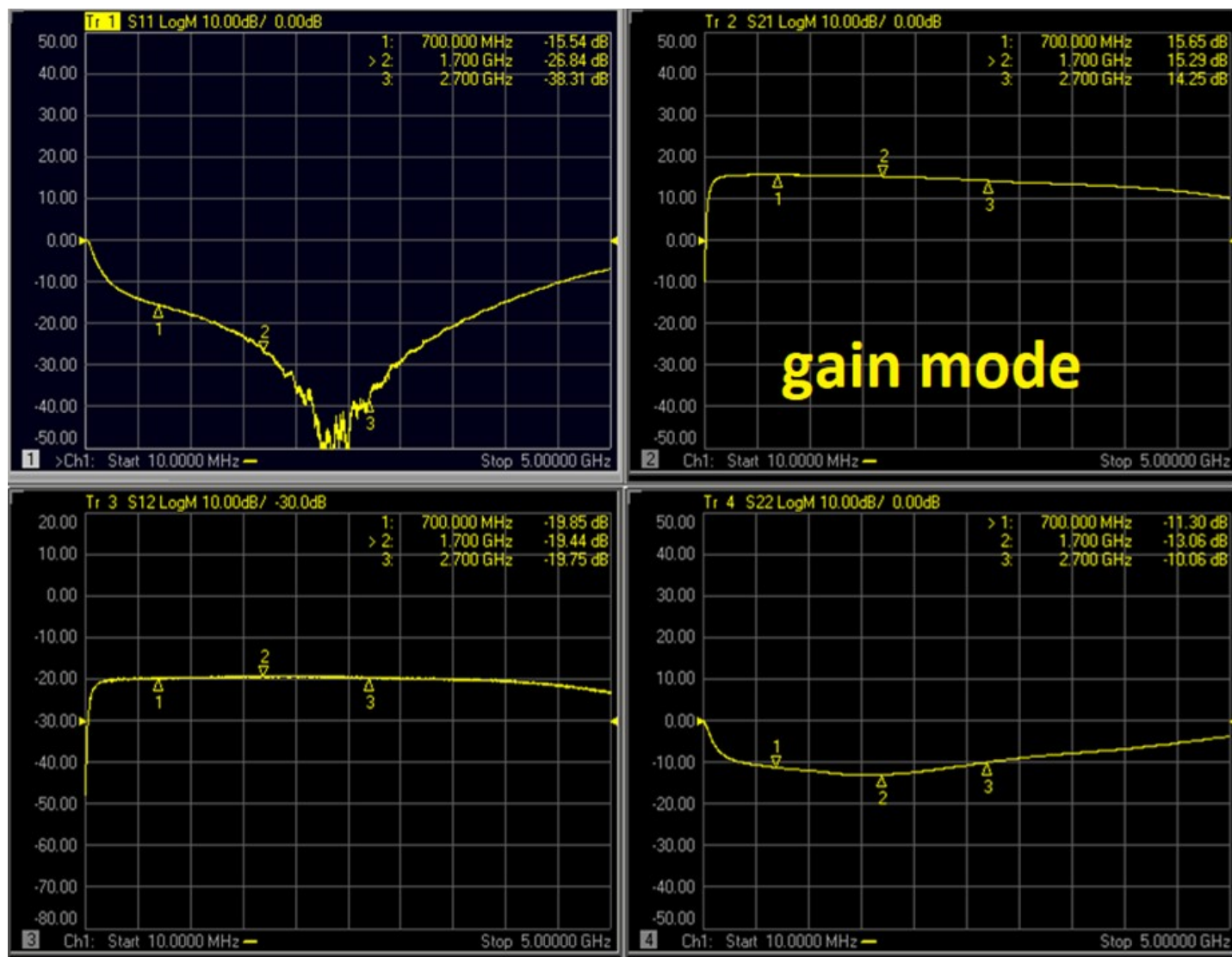
GRF2042 OIP3 vs Frequency at  $V_{en}(V) = 0 V$



## GRF2042 Typical Operating Curves: 0.7 to 2.7 GHz Tune



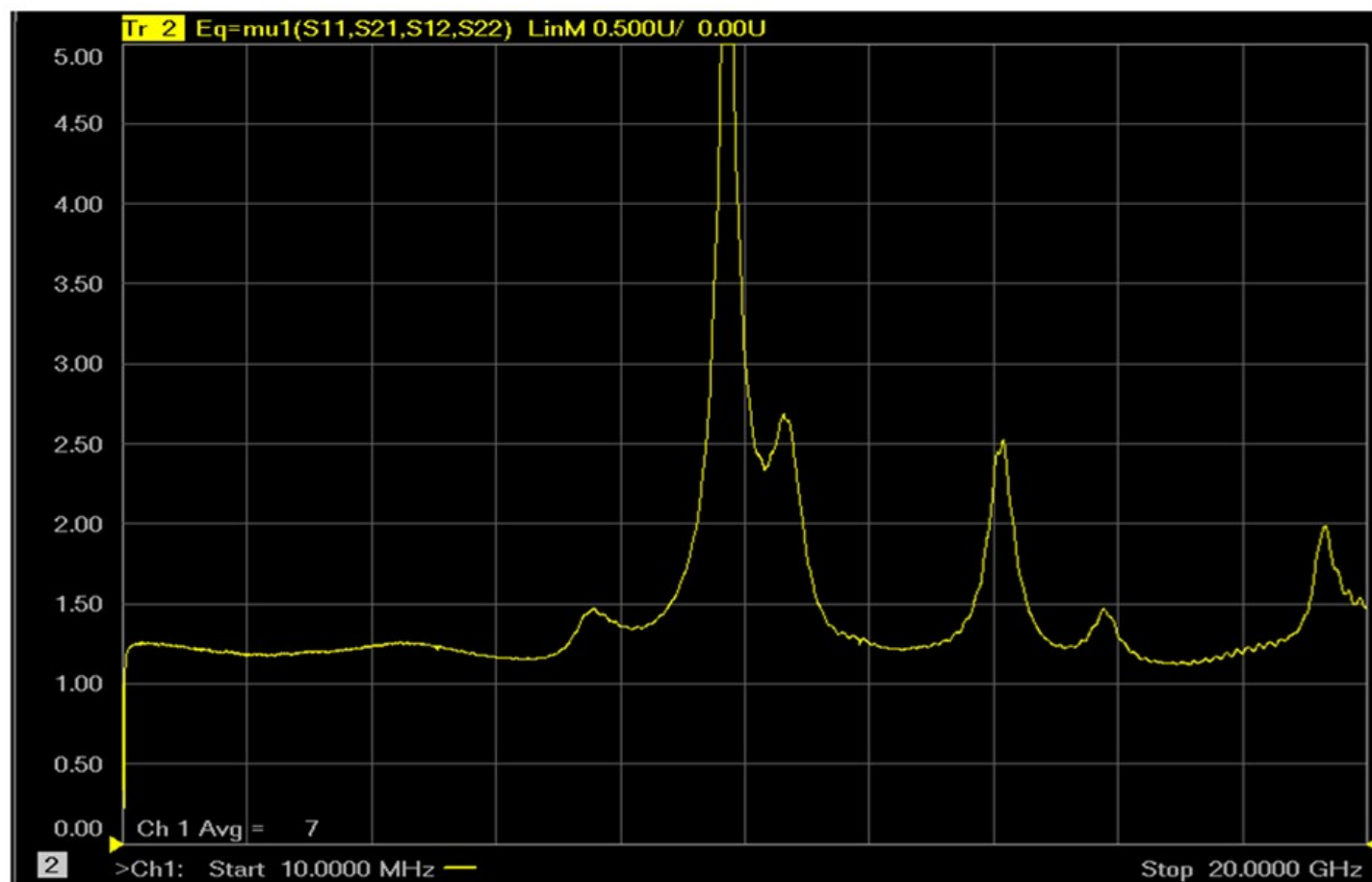
## GRF2042 Typical Operating Curves: S-Parameters (Gain Mode: 0.7 to 2.7 GHz Tune)



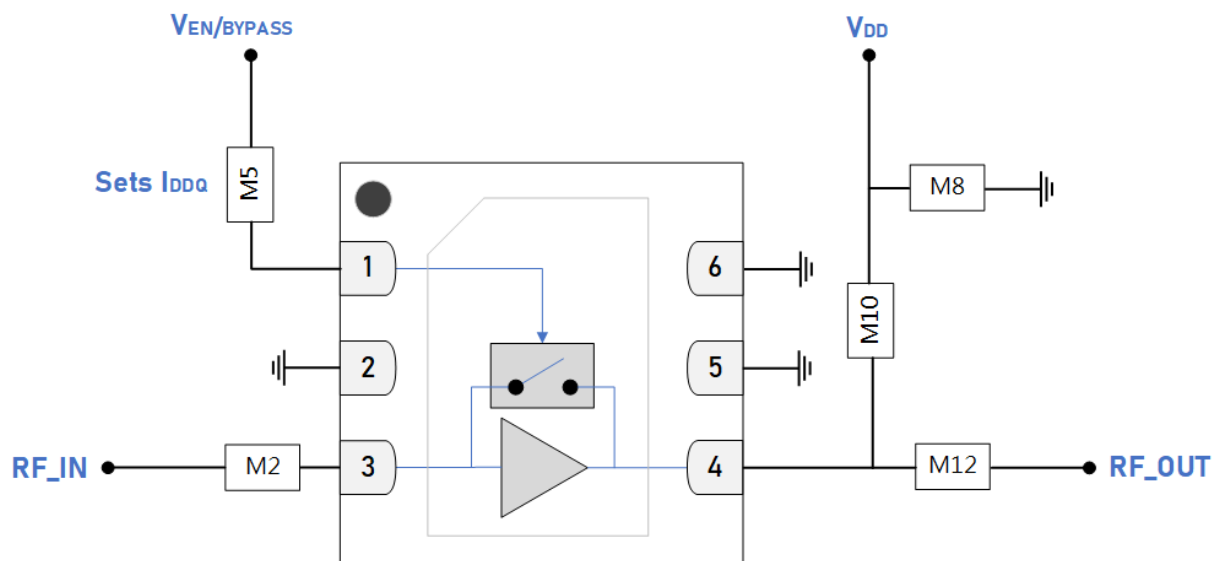
## GRF2042 Typical Operating Curves: S-Parameters (Bypass Mode: 0.7 to 2.7 GHz Tune)



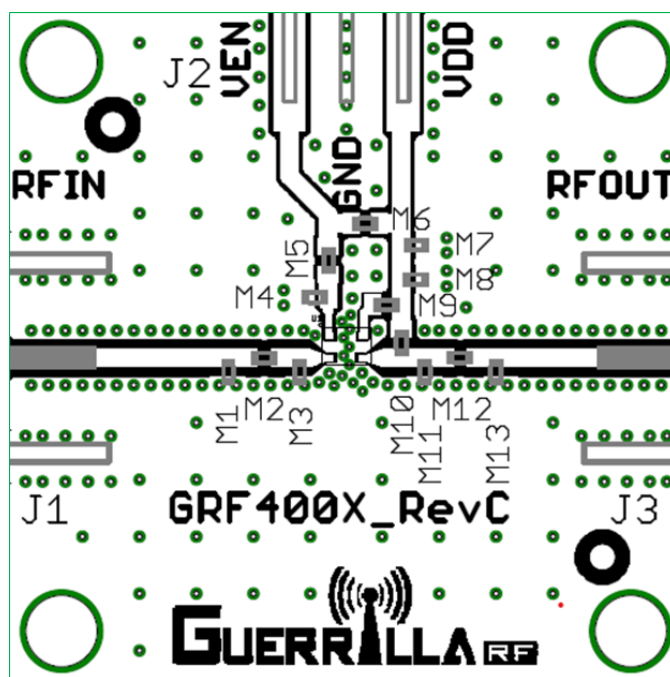
## GRF2042 Typical Operating Curves: Stability Mu Factor (10 MHz to 20 GHz)



Note: Mu Factor  $\geq 1.0$  implies unconditional stability.



**GRF2042 Standard Evaluation Board Schematic**



**GRF2042 Evaluation Board Assembly Diagram**

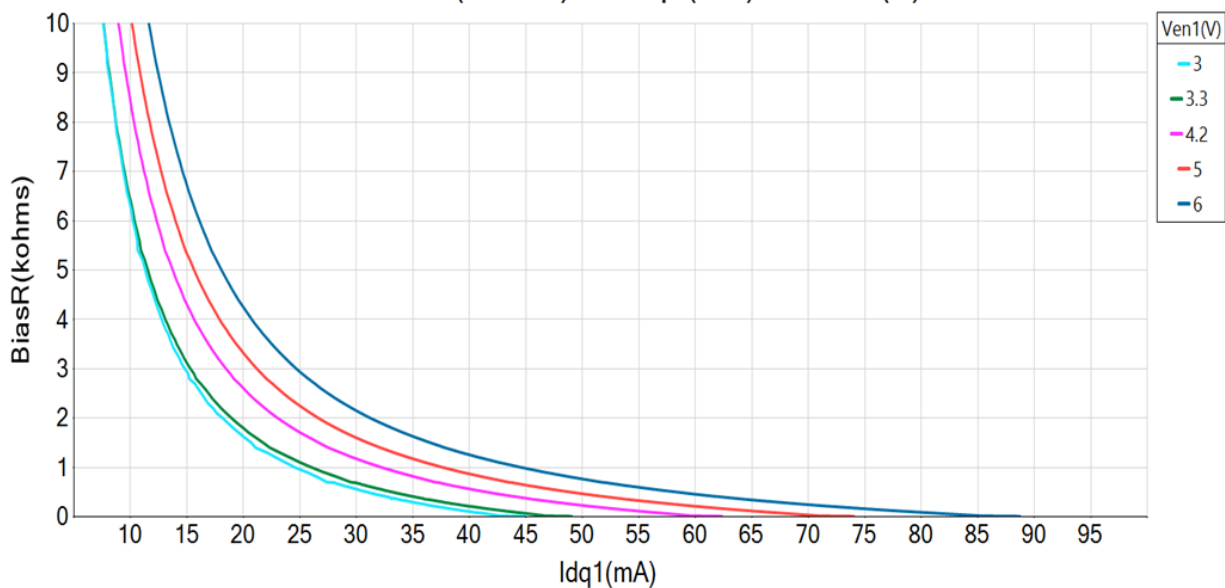
**GRF2042 Evaluation Board Assembly Diagram Reference: 0.7 to 2.7 GHz Tune**

Component	Type	Manufacturer	Family	Value	Package Size	Substitution
M1, M3, M4, M6, M7, M9, M11, M13	DNP	--	--	--	--	--
M2	Capacitor	Murata	GJM	100 pF	0402	ok
M5 (sets I <sub>DDQ</sub> )	Resistor	Various	5%	See Curves	0402	ok
M8	Capacitor	Murata	GRM	0.1 µF	0402	ok
M10	Inductor	Murata	LQG	47 nH	0402	ok
M12	Capacitor	Murata	GRM	100 pF	0402	ok
Evaluation Board	GRF400X_RevC					

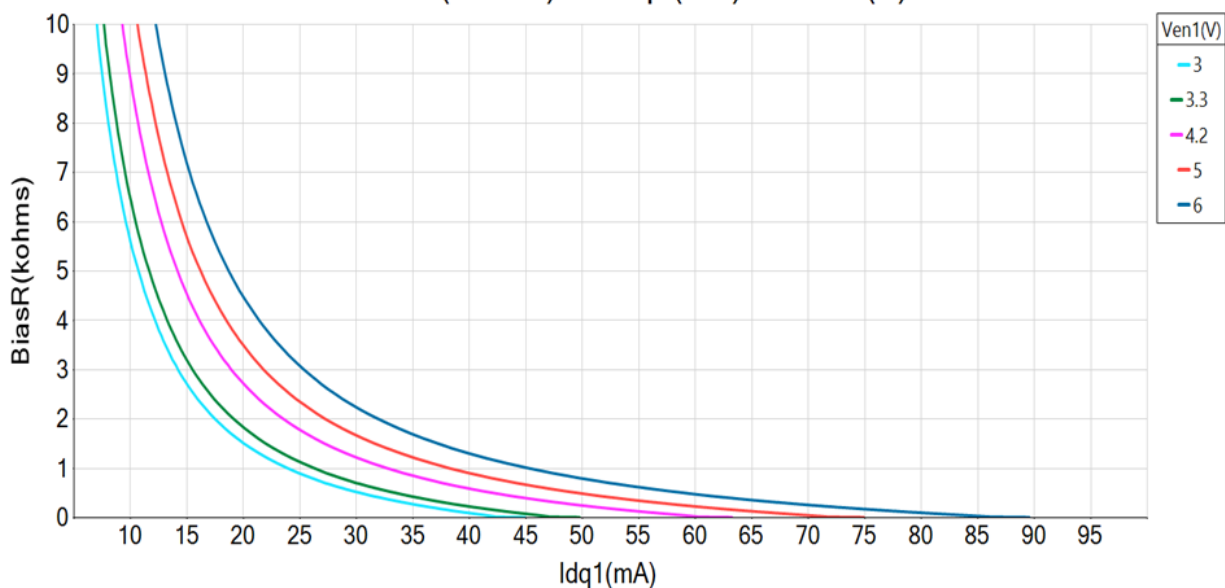


## GRF2042 Bias Resistor Selection Curves

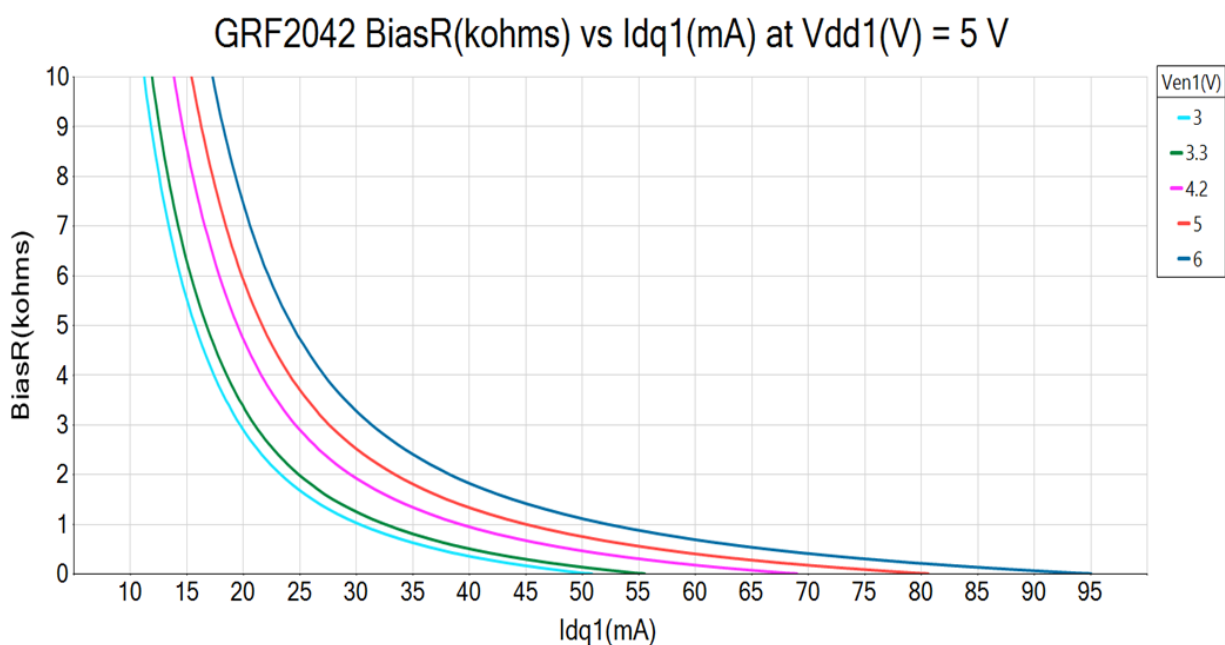
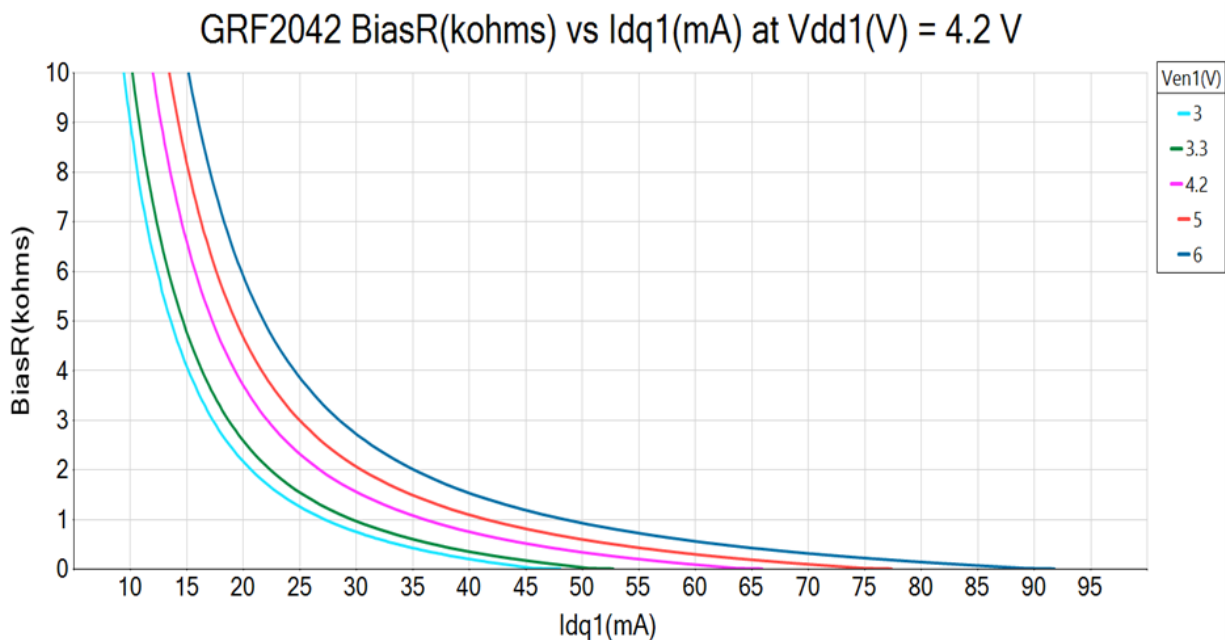
GRF2042 BiasR(kohms) vs Idq1(mA) at Vdd1(V) = 3 V



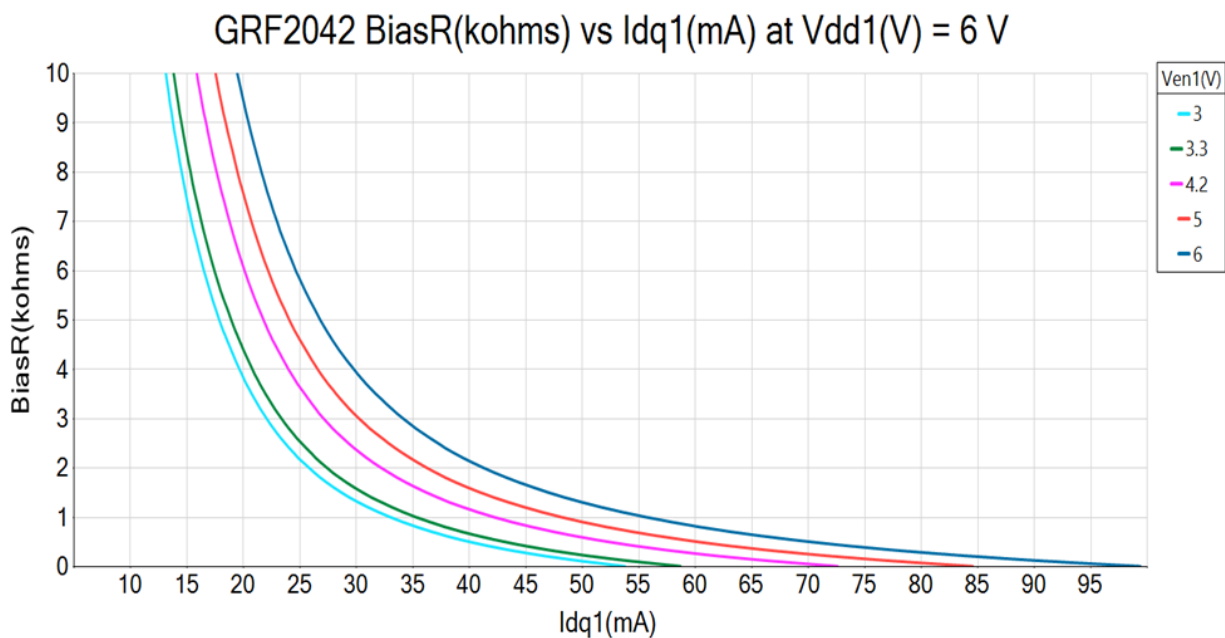
GRF2042 BiasR(kohms) vs Idq1(mA) at Vdd1(V) = 3.3 V

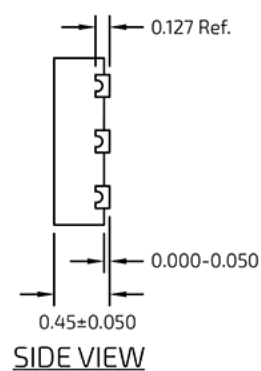
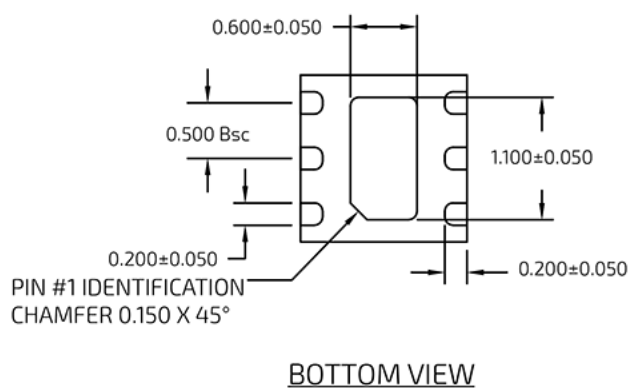
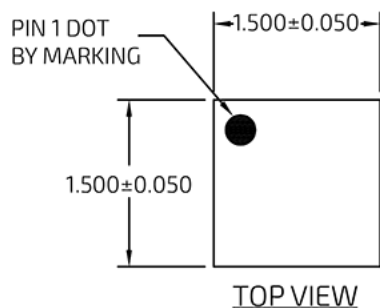


## GRF2042 Bias Resistor Selection Curves

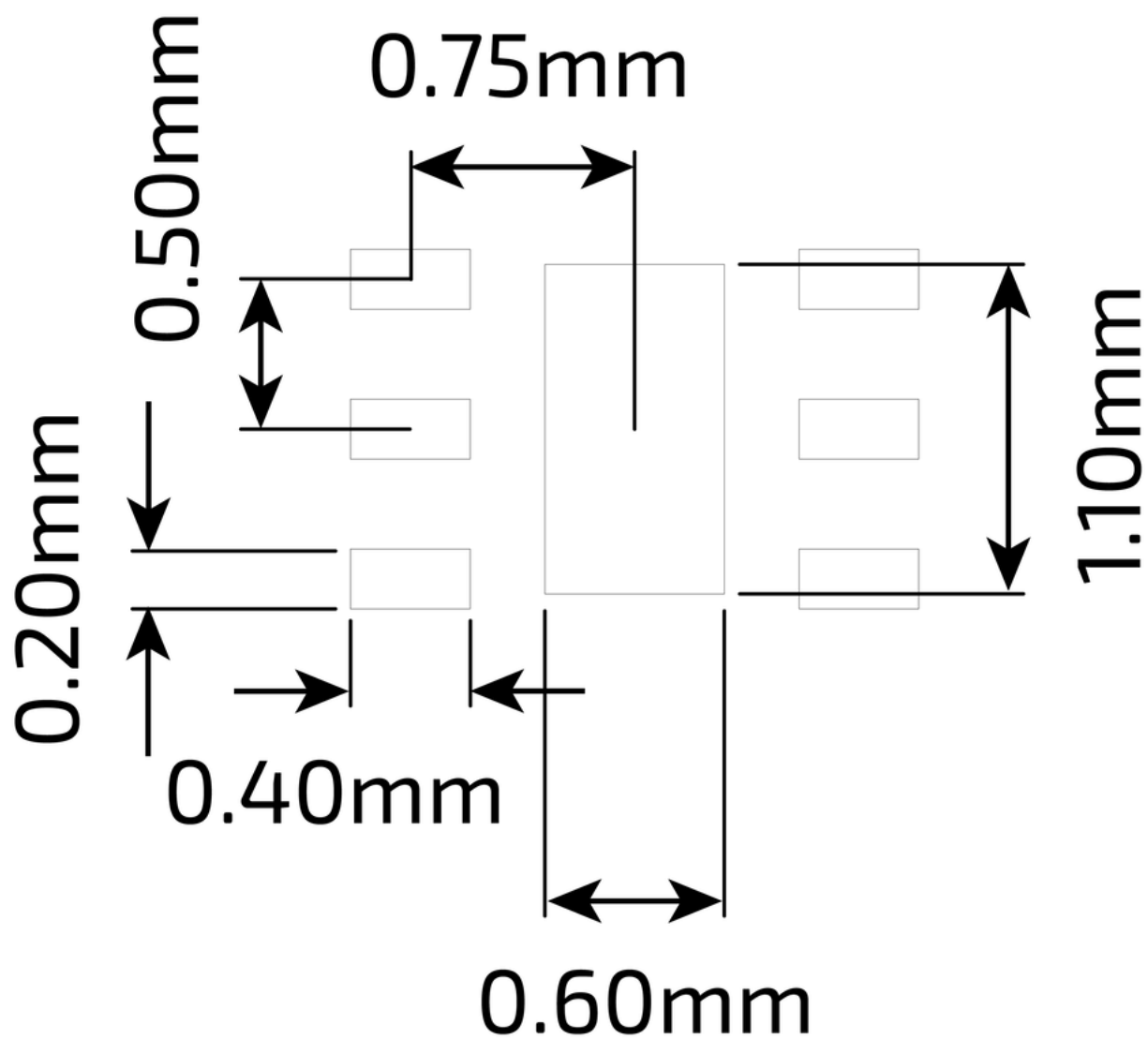


## GRF2042 Bias Resistor Selection Curves





### DFN 6 1.5x1.5mm Package Dimensions



DFN 6 1.5x1.5mm Suggested PCB Footprint (Top View)

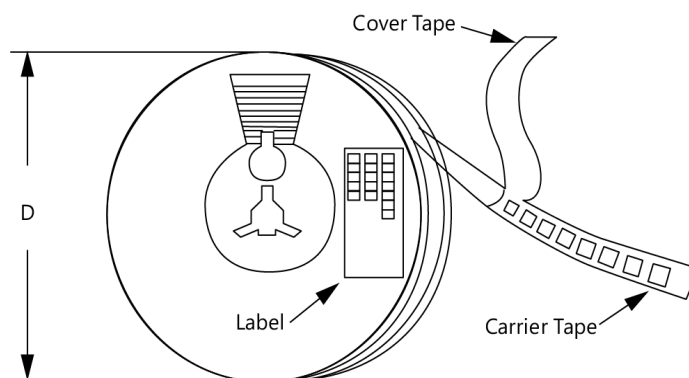
## Package Marking Diagram



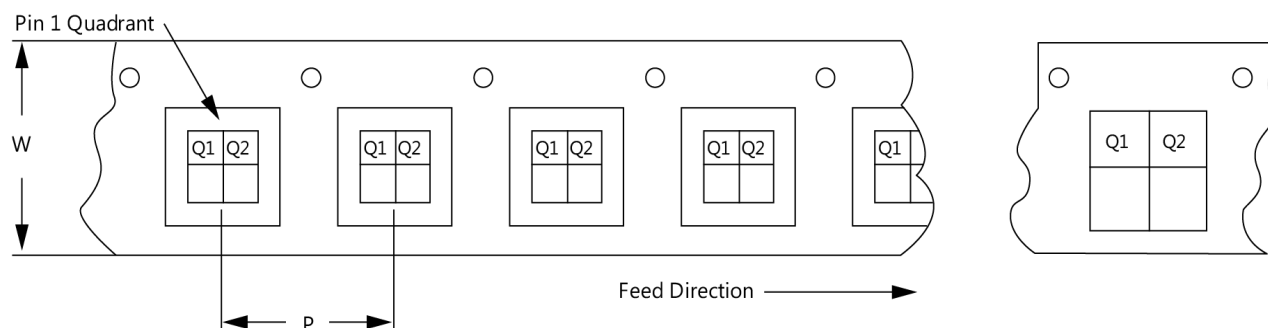
Line 1: "Y" = YEAR (single digit). "WW" = WORK WEEK the Device was assembled.  
Line 2: "XXXX" = Device Part Number.

## Tape and Reel Information

Guerrilla RF's tape and reel specification complies with Electronics Industries Association (EIA) standards for "Embossed Carrier Tape of Surface Mount Components for Automatic Handling" (reference EIA-481). Devices are loaded with pins down into the carrier pocket with protective cover tape and reeled onto a plastic reel. Each reel is packaged in a cardboard box. There are product labels on the reel, the protective ESD bag, and the outside surface of the box. For the latest reel specifications and package information (including units/reel), please visit [Package Manufacturing Information](#) | [Guerrilla RF](#) ([guerrilla-rf.com](http://guerrilla-rf.com)).



Tape and Reel Packaging with Reel Diameter Noted (D)



Carrier Tape Width (W), Pitch (P), Feed Direction and Pin 1 Quadrant Information



## Revision History

Revision Date	Description of Change
March 2, 2020	Preliminary Data Sheet.
November 18, 2022	Release Ø data sheet. Updated Data Sheet to new format. Added new characterization plots. Added new bias resistor selection curves.
November 28, 2022	Updated pin assignments table.
December 20, 2022	Raised RF Input Power (PIN MAX) from 10 to 27 dBm in Gain Mode. Added RF Input Power (PIN MAX) in Bypass Mode.
October 4, 2023	Updated Data Sheet to newest format only.



## Data Sheet Classifications

Data Sheet Status	Notes
Advance	S-parameter and NF data based on EM simulations for the fully packaged device using foundry-supplied transistor S-parameters. Linearity estimates based on device size, bias condition and experience with related devices.
Preliminary	All data based on evaluation board measurements taken within the Guerrilla RF Applications Lab. Any MIN/MAX limits represented within the data sheet are based solely on <i>estimated</i> part-to-part variations and process spreads. All parametric values are subject to change pending the collection of additional data.
Release Ø	All data based on measurements taken with <i>production-released</i> material. TYP values are based on a combination of ATE and bench-level measurements, with MIN/MAX limits defined using <i>modelled estimates</i> that account for part-to-part variations and expected process spreads. Although unlikely, future refinements to the TYP/MIN/MAX values may be in order as multiple lots are processed through the factory.
Release A-Z	All data based on measurements taken with production-released material <i>derived from multiple lots which have been fabricated over an extended period of time</i> . MIN/MAX limits may be refined over previous releases as more statistically significant data is collected to account for process spreads.

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